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JUL 19 2004

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Urgent

Confidential

Date: 19-Jul-04

To:
Examiner: Eric Chang
USPTO

Fax:
(703) 872-9306

Art Unit:
2116

From:
Thomas R. Lane
Intel Corporation

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M/S:
CY-LF3

Subject: Application No.: 09/504,003

Filed:
February 14, 2000

Inventors:
Kahn, et al

Docket No.:
42390P8517

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Thomas R. Lane Date: 7/19/04 *TR Lane*

Included in this transmission:

Fax Cover Sheet (1 page)

Response to Final Office Action (4 pages)

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42390P8517

PATENT

#15
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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JUL 19 2004

In re Application of:)

Kahn, et al)

Examiner: Chang, Eric)

Serial No.: 09/504,003)

Art Unit: 2116)

Filing Date: February 14, 2000)

For: (As Amended) A METHOD AND)
APPARATUS FOR RESUMING)
MEMORY OPERATIONS UPON EXIT)
FROM A LOW LATENCY WAKE-UP)
LOW POWER STATE)

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CERTIFICATE OF MAILING/TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date indicated below and that this paper or fee has been addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, fax number 703-872-9306.

Date of Deposit: July 19, 2004

Name of Person Transmitting Correspondence: Thomas R. Lane

Signature

Date

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed on May 20, 2004, the applicants respectfully request consideration of the following remarks.